

ISCS - Bug #1416

SHL15 128 QTISCS (0x80 ehdc 0-127 0x01)

06/01/2021 03:19 PM - yufeng wu

|                                                                              |          |                 |            |
|------------------------------------------------------------------------------|----------|-----------------|------------|
| Status:                                                                      | Resolved | Start date:     | 06/01/2021 |
| Priority:                                                                    | High     | Due date:       |            |
| Assignee:                                                                    |          | % Done:         | 0%         |
| Category:                                                                    |          | Estimated time: | 0.00 hour  |
| Target version:                                                              |          | Spent time:     | 0.00 hour  |
| Description                                                                  |          |                 |            |
| QTISCS3.03<br>pscada--FEP--QTISCS<br>SHL15 128 QTISCS (0x80 ehdc 0-127 0x01) |          |                 |            |
| Related issues:                                                              |          |                 |            |
| Related to ISCS#FEP - Bug #1417:                                             |          | Resolved        | 06/01/2021 |

History

#1 - 06/01/2021 05:26 PM - yufeng wu

- Priority changed from Normal to High

100%

#2 - 06/01/2021 06:16 PM - xiangyang li

- Related to Bug #1417: added

#3 - 06/02/2021 03:35 PM - yufeng wu

- File bin15sp304+acc.zip added

- Status changed from New to Resolved

QTISCS3.0.4 pooldatabase syndaqs  
dac acc  
ehrawdc bug

Files

|                    |         |            |           |
|--------------------|---------|------------|-----------|
| bin15sp304+acc.zip | 55.8 KB | 06/02/2021 | yufeng wu |
|--------------------|---------|------------|-----------|